CONNECTION DIAGRAM and PIN DEFINITION - 8 BIT MICROPROCESSOR - M 380

SYNC	Initial reset/synchronization (input)
CLOCK	System clock (input)
DAB 8N ..... DAB 1N	8 bit bidirectional data-bus
PBO 8N ..... PBO 5N	4 bit input-output peripheral-bus
PBO 4N ..... PBO 1N	4 bit input peripheral bus
ADB 6X ..... ADB 1X	6 bit address bus (outputs)
DBDI	Data bus direction
ADSL	Address/data select (4 control)
WEQZ	Write enable a or Z (lines (outputs))
PPOP	Push/pop operation
X	Data true if logic level is 1
N	Data true if logic level is 0

LPS000 LOGIC PROCESSOR

Pin Connections

1	Vcc
2	Power on Reset
3	Clock
4	Not Used
5	" "
6	Data Bus 8
7	" " 7
8	" " 6
9	" " 5
10	" " 4
11	" " 3
12	" " 2
13	" " 1
14	Peripheral Bus 8
15	" " 7
16	" " 6
17	" " 5
18	" " 4
19	" " 3
20	" " 2
21	Peripheral Bus 1
22	Address Bus 1
23	" " 2
24	" " 3
25	" " 4
26	" " 5
27	" " 6
28	Not Used
29	" "
30	" "
31	" "
32	" "
33	" "
34	" "
35	CIO
36	CDA
37	CQZ
38	CRA
39	Vgg
40	Vgi